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Patent 5448568: System of transmitting an interactive TV signal

... of signal modules is a computer which **inserts** time stamps in said signal ...  
8 and 9 are signal waveforms representing alternative **timing** sequences for ...  
[www.freepatentsonline.com/5448568.html](http://www.freepatentsonline.com/5448568.html) - 55k - Cached - Similar pages

Patent 4499578: Method and apparatus for controlling signal level ...

... The interface selects the 64 out of 256 time **slots** required for the audio ...  
formats the data, and generates **timing signals** for the echo cancelers, ...

[www.freepatentsonline.com/4499578.html](http://www.freepatentsonline.com/4499578.html) - 100k - Cached - Similar pages

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... the 21264 **inserts** the true branch direction in the local history **table** and  
updates the ... where **timing signals** are sent in parallel with data **signals**, ...  
[h21007.www2.hp.com/dspp/tech/tech\\_TechDocumentDetailPage\\_IDX/1,1701,4831,00.html](http://h21007.www2.hp.com/dspp/tech/tech_TechDocumentDetailPage_IDX/1,1701,4831,00.html) - 119k -  
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... AC Alternating current. The frequency is measured in cycles per seconds (cps)  
... **Timing** is dependent on the actual time for the transfer to take place, ...  
[cma.zdnet.com/book/upgraderepair/apb/apb.htm](http://cma.zdnet.com/book/upgraderepair/apb/apb.htm) - 194k - Cached - Similar pages

EP428021 Compaq european software patent - Method for data ...

... **Table** of contents: Bibliography; Abstract; Description; Claims; Comment;  
Sources ... of the EISA bus clock **signal** that governs EISA bus 46 **timing**. ...  
[gauss.ffii.org/PatentView/EP428021](http://gauss.ffii.org/PatentView/EP428021) - 158k - Cached - Similar pages

Bandwidth Market, Ltd

... up/down counter to read "10," the status update of the ... lines, thereby eliminating  
the need to **search** the busy ... information is inhibited as shown in **Table 4** by ...  
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... otherwise the bus interface controller **inserts** all one's fill ... generated **timing**  
**signals** enable the write of the first byte of a frame into the R/S ...  
[www.tw.i-3com.com/telemetry\\_west/products/datasheets/SECTION\\_1.pdf](http://www.tw.i-3com.com/telemetry_west/products/datasheets/SECTION_1.pdf) - Similar pages

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... To guarantee proper **signal timing**, a 100BaseFX link exceed 400 meters in ...  
A measure of the opposition to the flow of an **alternating signal** by its ...  
[www.wildpackets.com/resources/glossary](http://www.wildpackets.com/resources/glossary) - 277k - Cached - Similar pages

Digital multi-customer <a href=".../..../indices/keyword/52/">data ...

... Concurrently filed herewith and assigned to the same assignee as this application  
... The **timing** of flop-flop 804 is controlled via a **timing signal** on ...  
[patent.tange.dk/ziki/EP/1/3/EP139687.html](http://patent.tange.dk/ziki/EP/1/3/EP139687.html) - 324k - Cached - Similar pages

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... tem be synchronized to a single global **timing signal** as with clocked ...

**Table 6.1** shows the bits needed for each of the **IW slots** in the current VHDL ...

[www.cs.utah.edu/techreports/1996/pdf/CSTD-96-001.pdf](http://www.cs.utah.edu/techreports/1996/pdf/CSTD-96-001.pdf) - [Similar pages](#)

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